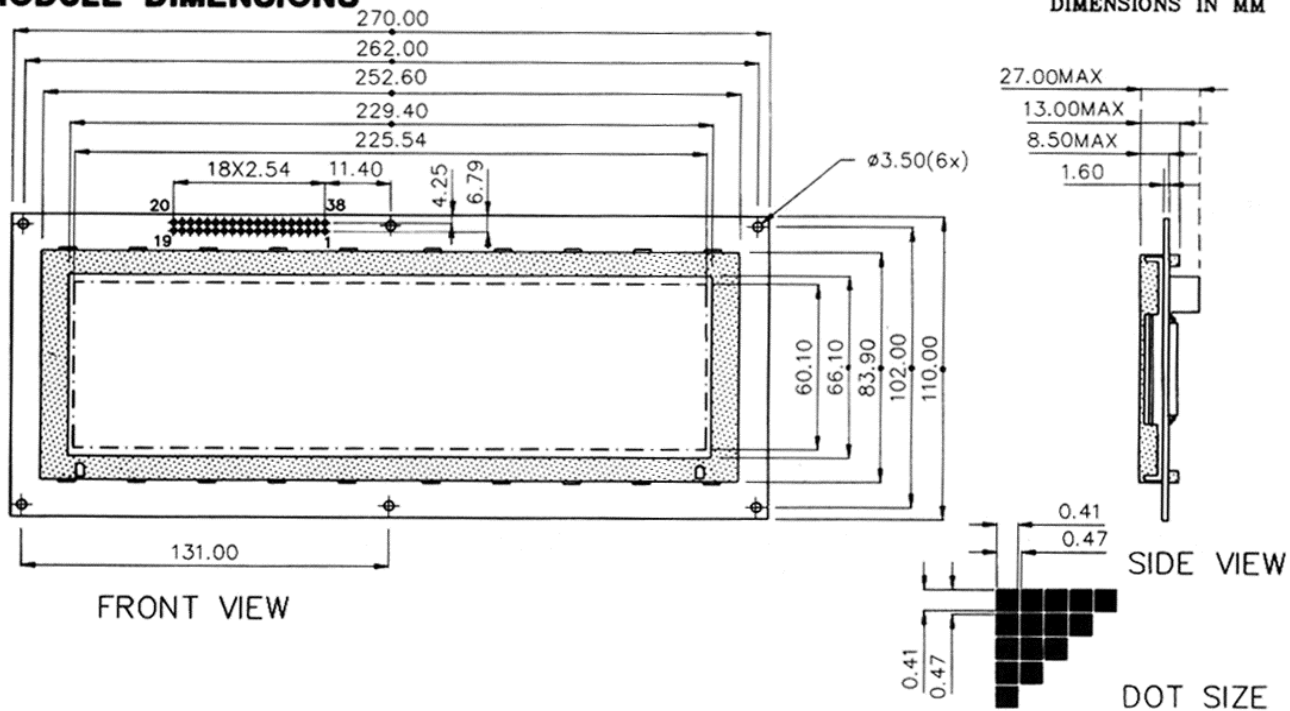
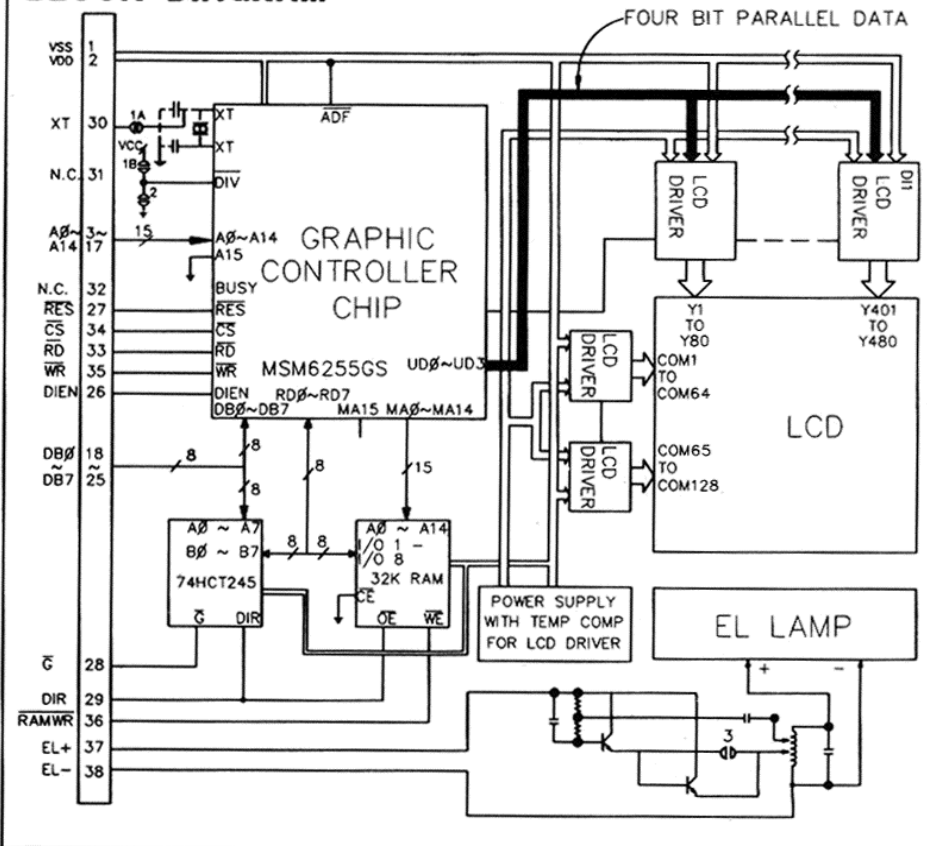


MODULE DIMENSIONS

DIMENSIONS IN MM



BLOCK DIAGRAM



PIN-OUT ASSIGNMENT

PIN NO.	SYMBOL	LEVEL	FUNCTION
1	VSS	-	logic ground.
2	VDD	+5V	Module power supply
3~17	A β ~A γ	H/L	15-Lines address bus
18~25	DB β ~DB γ	H/L	8-bit data bus
26	DIEN	H	DISPLAY ENABLE DIEN='H'...Display is enable Display is disable DIEN='L'... Display data can be directly read from or written into the display RAM.
27	RES	L	Reset
28	G	L	Tri-state buffer enable pin.
29	DIR	H/L	DIR=0... DIR=1...
30	XT	-	XTAL OSC
31	NC	-	No connection
32	NC	-	No connection
33	RD	L	Read data from controller
34	CS	L	Chip select.
35	WR	L	Write data to controller
36	RAMWR	L	Write data to display RAM.
37	EL+	+5V	Supply for EL driver circuitry
38	EL-	-	EL Ground

ABSOLUTE MAXIMUM RATING

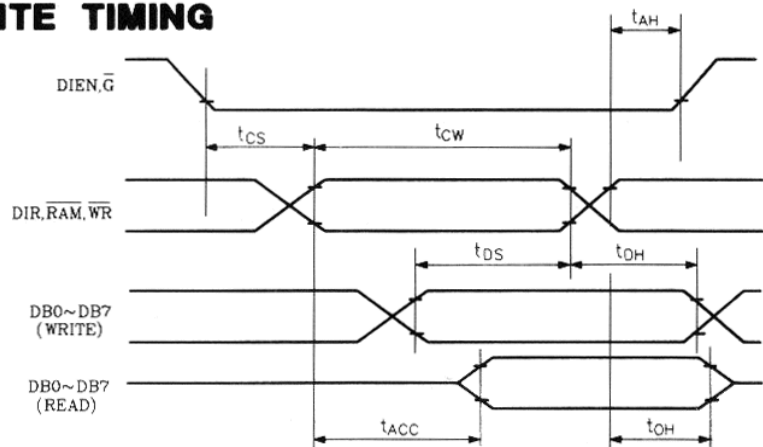
NO.	PARAMETER	SYMBOL	MIN	MAX	UNIT
1.	Power supply for logic	VDD - VSS	∅	7.0	V
2.	Power supply for LCD drive	VDD - VNEG	∅	24.0	V
3.	Input voltage	VI	VSS	VDD	V
4.	Operating temperature	Ta	0	50	°C
5.	Storage temperature	Tstg	-20	70	°C

ELECTRICAL CHARACTERISTICS (Ta = +25°C)

NO.	PARAMETER	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT
1.	Supply voltage	VDD	-	4.5	5.0	5.5	V
2.	Module supply current	IDD	VDD = 5.0V	-	15	25	mA
3.	High level input voltage for logic	VIH	-	2.4	-	VDD	V
4.	Low level input voltage for logic	VIL	-	0	-	0.7	V
5.	High level output voltage for logic	VOH	- IOH = 0.6mA	VDD - 0.4	-	VDD	V
6.	Low level output voltage for logic	VOL	IOL = 1.6mA	0	-	0.4	V
7.	VEE, Negative supply	VNEG	VDD = 5.0V	-15	-	-18	V
8.	EL+, EL driver input voltage	VEL+	-	-	5.0	-	V
9.	EL supply current	VEL+	EL+ = 5.0V	-	80	100	mA

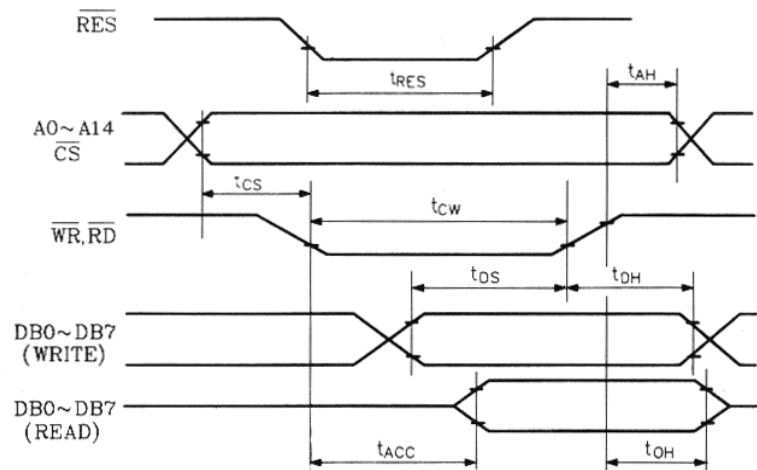
DISPLAY RAM---READ/WRITE TIMING

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
DIEN,G CS set up time	tCS	100	-	-	nS
DIR,RAMER pulse width	tcw	200	-	-	nS
Address hold time	tAH	40	-	-	nS
Data set up time	tDS	200	-	-	nS
Data hold time	tDH	40	-	-	nS
Output disable time	tOH	0	-	40	nS
Access time	tACC	-	-	200	nS



MSM6255 READ/WRITE TIMING

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Reset low level pulse width	tRES	1	-	-	μS
Address bus, CS set up time	tCS	100	-	-	nS
RD,WR pulse width	tcw	300	-	-	nS
Address hold time	tAH	40	-	-	nS
Data set up time	tDS	200	-	-	nS
Data hold time	tDH	40	-	-	nS
Output disable time	tOH	0	-	40	nS
Access time	tACC	-	-	200	nS



NOTE : MUST SET DIEN SIGNAL TO HIGH.